



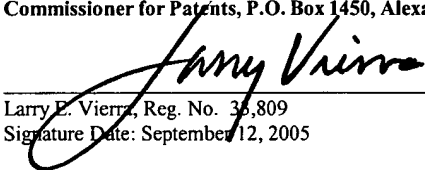
**RESPONSE UNDER 37 C.F.R. §1.116
EXPEDITED PROCEDURE - EXAMINING GROUP 2800**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application)	PATENT APPLICATION
Inventor: John Pasternak)	
Application No.: 10/633,110)	Art Unit: 2816
Filed: August 1, 2003)	Examiner: Tra, Anh Quan
Title: VOLTAGE REGULATOR WITH BYPASS FOR MULTI-VOLTAGE STORAGE SYSTEM)	Customer No. 28554

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail in an envelope addressed to **Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450**, on September 12, 2005



Larry E. Vierra, Reg. No. 38,809
Signature Date: September 12, 2005

RESPONSE B TO FINAL OFFICE ACTION UNDER 37 C.F.R. §1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This RESPONSE is in reply to the Office Action mailed May 11, 2005.

AMENDMENTS TO THE CLAIMS are reflected in the listing of claims which begin on page 2 of this Response.

REMARKS begin on Page 7 of this Response.